## SHEET 1 of 1

U.S. Department of Commerce
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Applicant

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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Filing Date	Group Art Unit
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			U.S. PAT	ENT DOCUMENTS			
*EXAMINER INITIALS	. /	DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01						

		F	OREIGN PA	TENT DOCUM	MENTS	<del>'''''''''''''''''''''''''''''''''''''</del>	
*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes No
	B01	)					

	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)				
ಯು	C01	Li et al., Wafer Level and Flip Chip Design Through Solder Prediction Models and Validation, Proceedings of the 7 <sup>th</sup> Mechatronics Forum International Conference (September, 2000).			
ಯ	C02	VCSEL (Vertical Cavity Surface Emitting Laser), http://www.lasermate.com (printed February 24, 2003).			
ಳಾವಿ	C03	VCSEL - A Whatis Definition, http://www.techtarget.com (printed February 24, 2003).			

EXAMINER

Christian Wisa

DATE CONSIDERED

7/19/05

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not cinsidered. Include copy of this form with next communication to applicant.